

FIG. 4 PRIOR ART

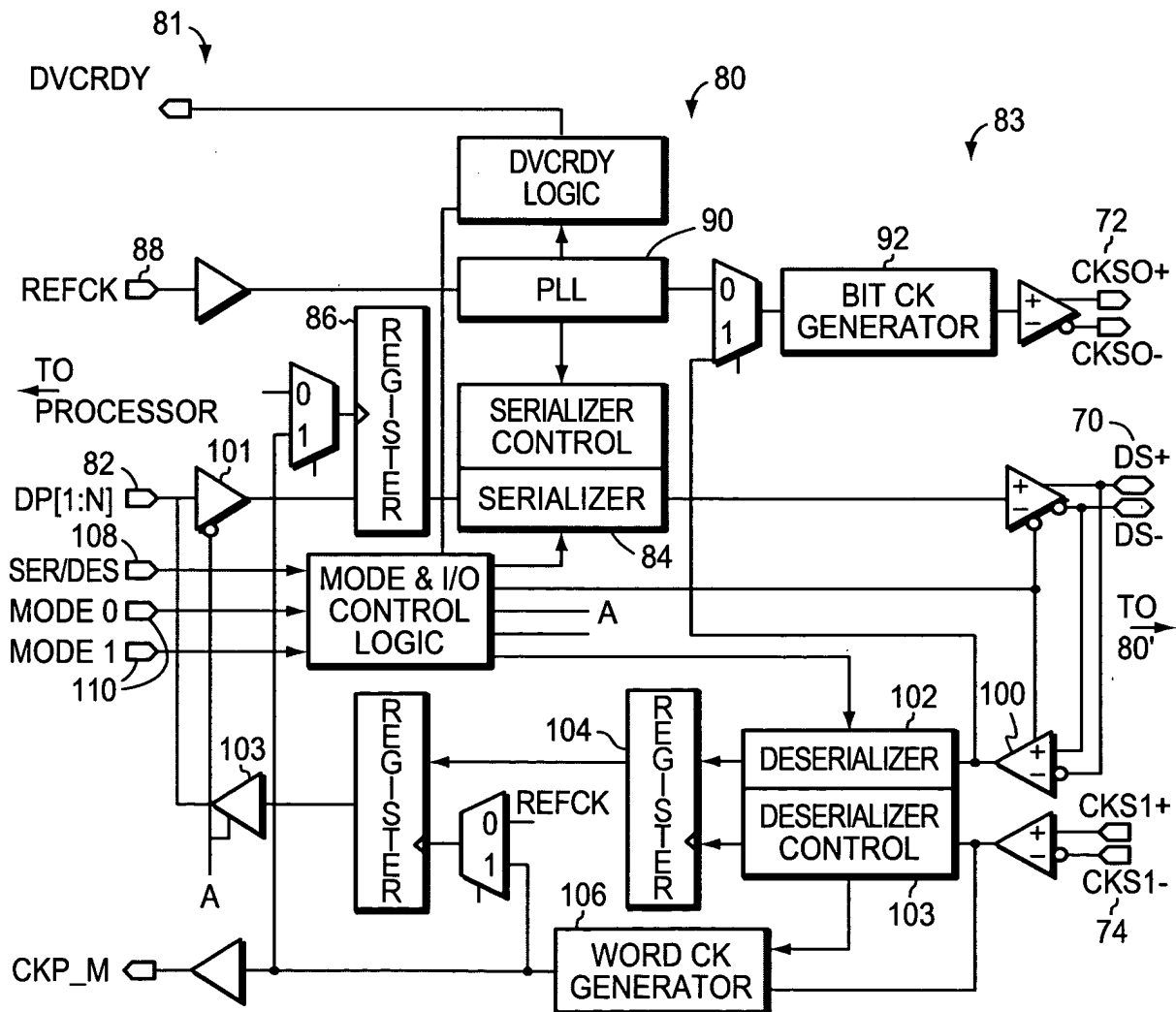


FIG. 5

MODE #	MODE0	MODE1	SER/DES	OPERATING MODE
0	0	0	X	POWER DOWN
1	0	1	0	DESERIALIZER PASSING MASTER CLOCK
1	0	1	1	SERIALIZER
2	1	0	0	DESERIALIZER
2	1	0	1	SERIALIZER WITH TURNAROUND CLOCK
3	1	1	0	DESERIALIZER
3	1	1	1	SERIALIZER

FIG. 6

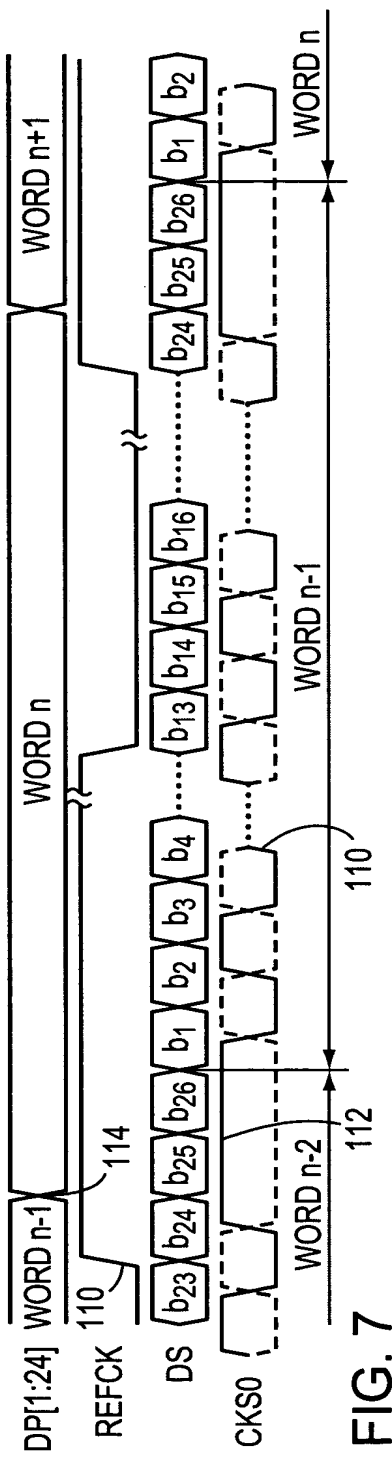


FIG. 7

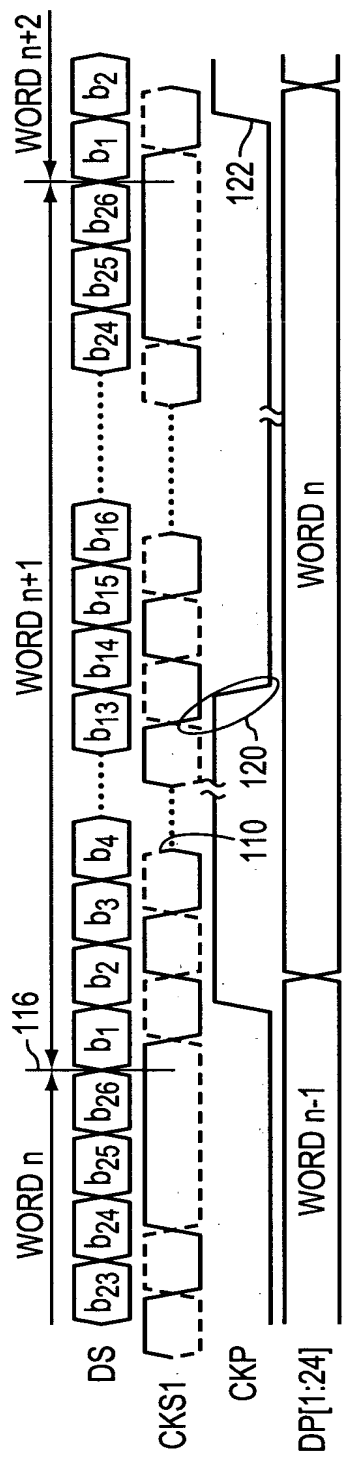


FIG. 8A



**FIG. 8B**

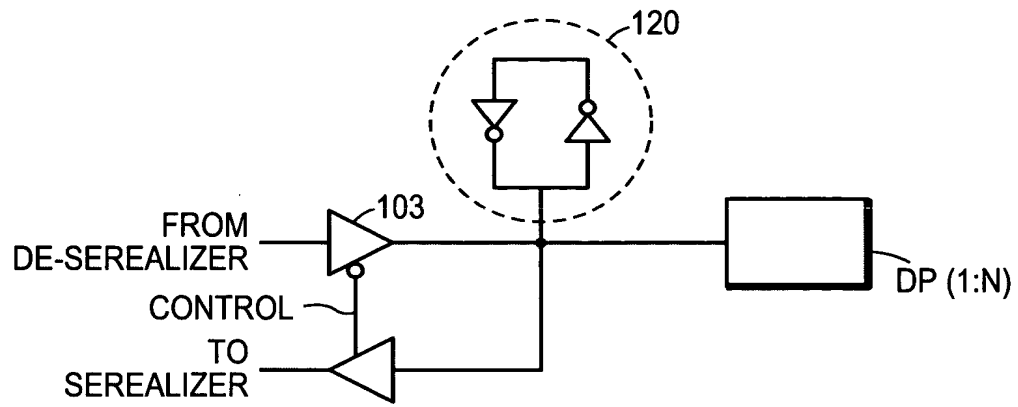


FIG. 9

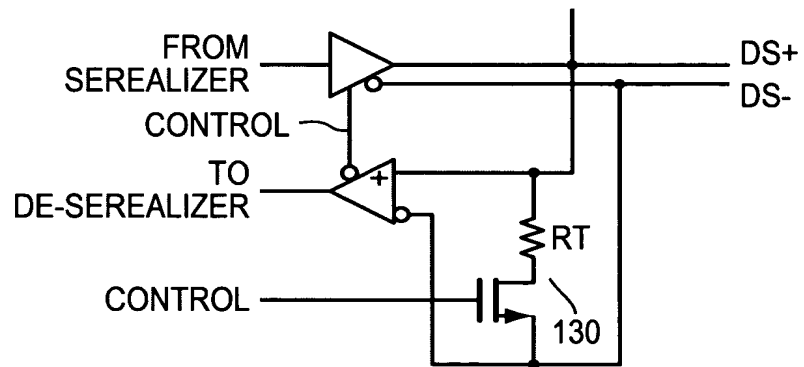


FIG. 10

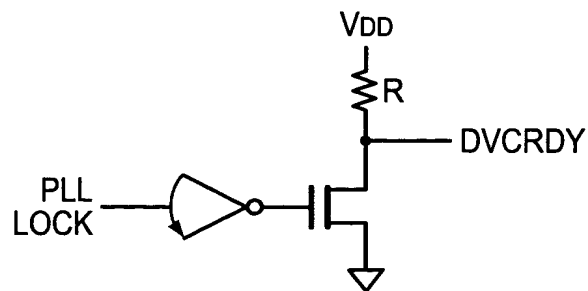


FIG. 11

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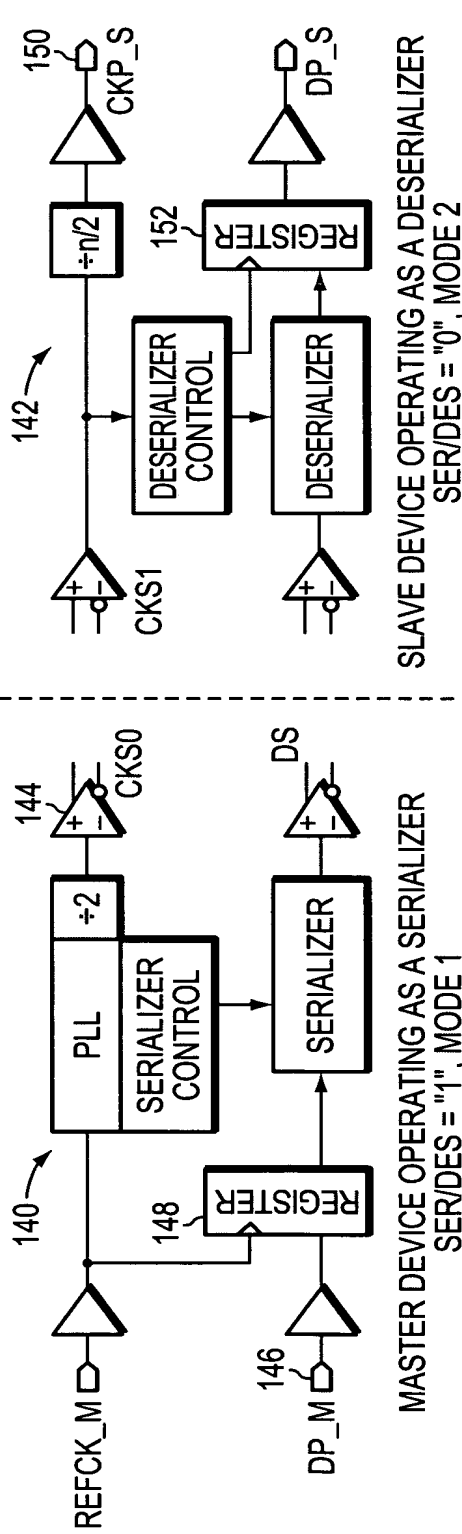


FIG. 12

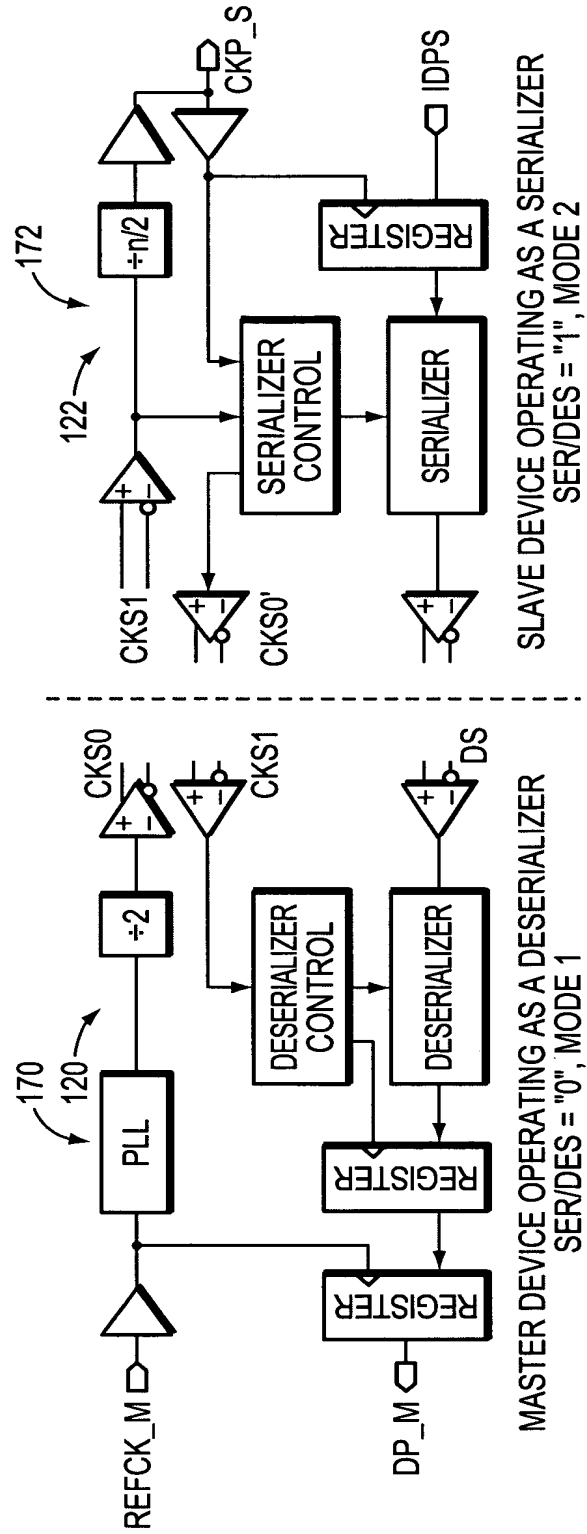


FIG. 13

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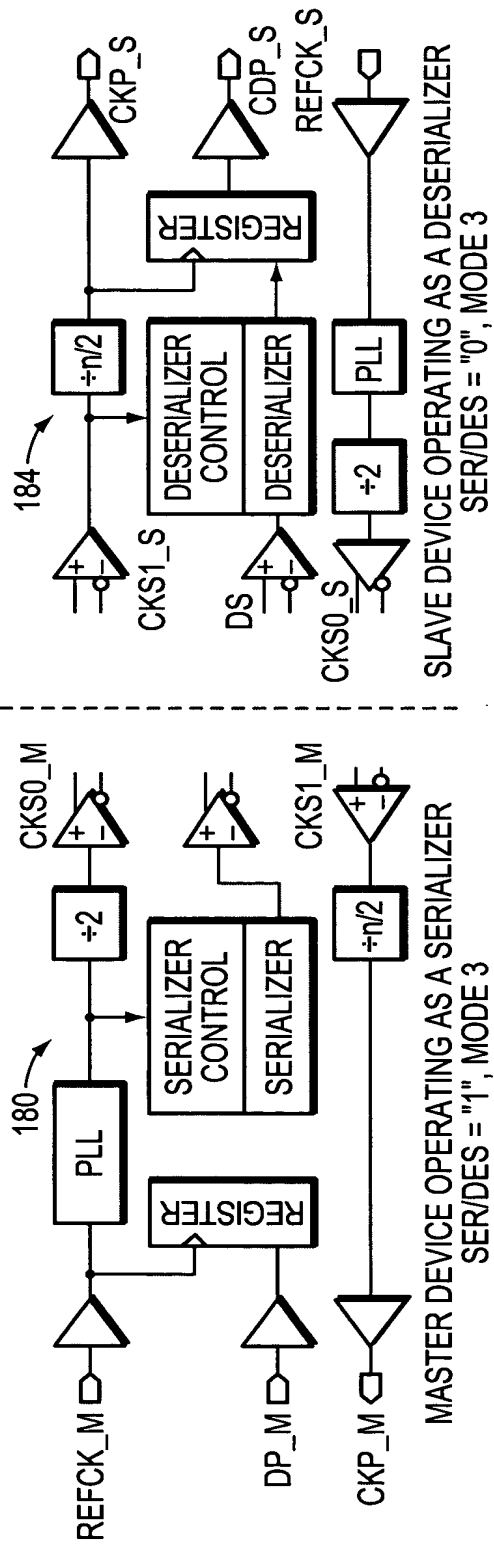


FIG. 14

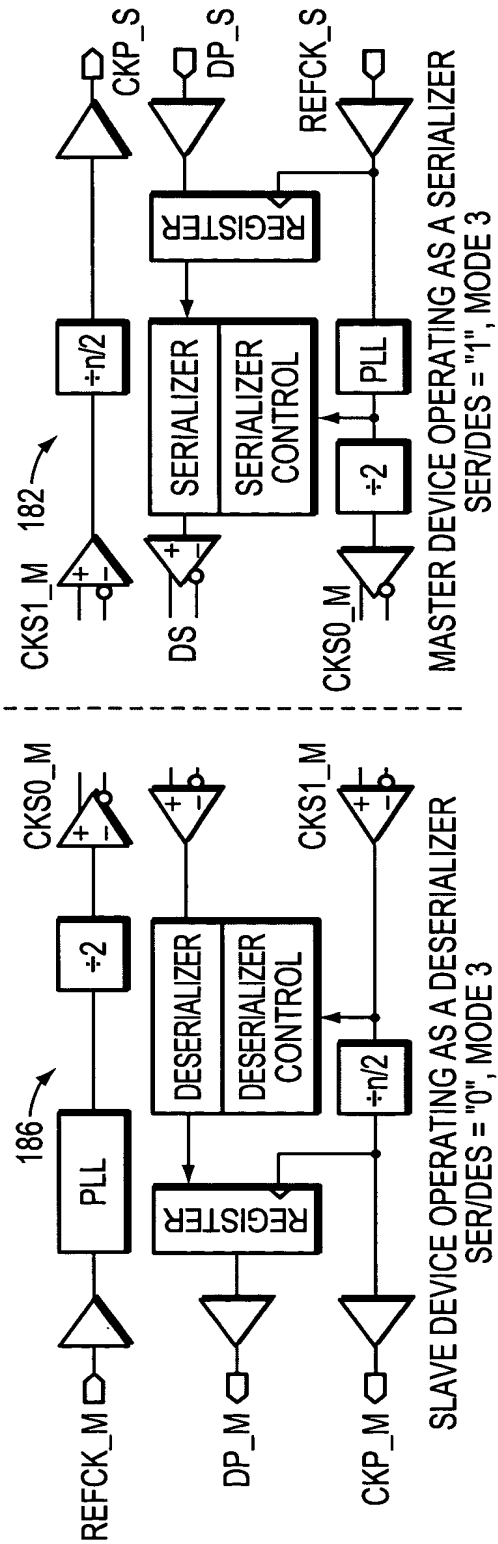


FIG. 15

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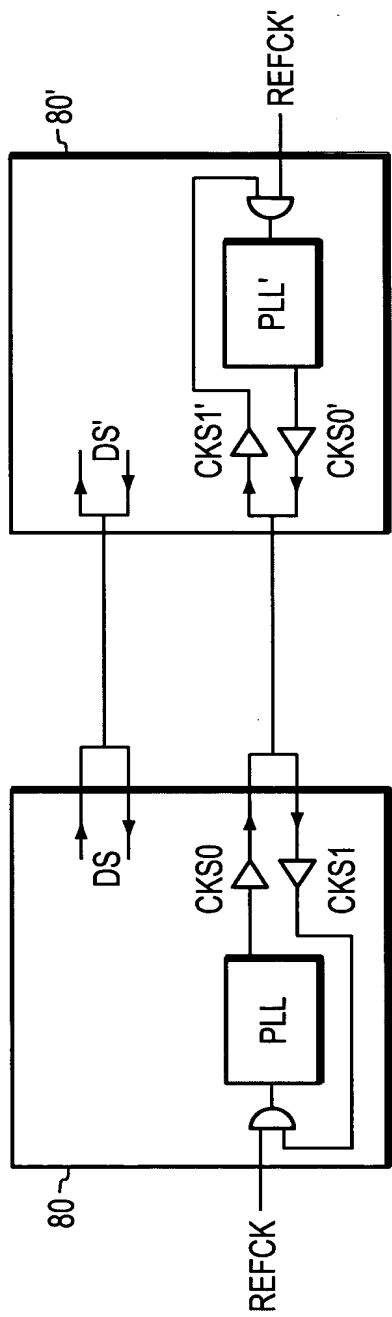


FIG. 16

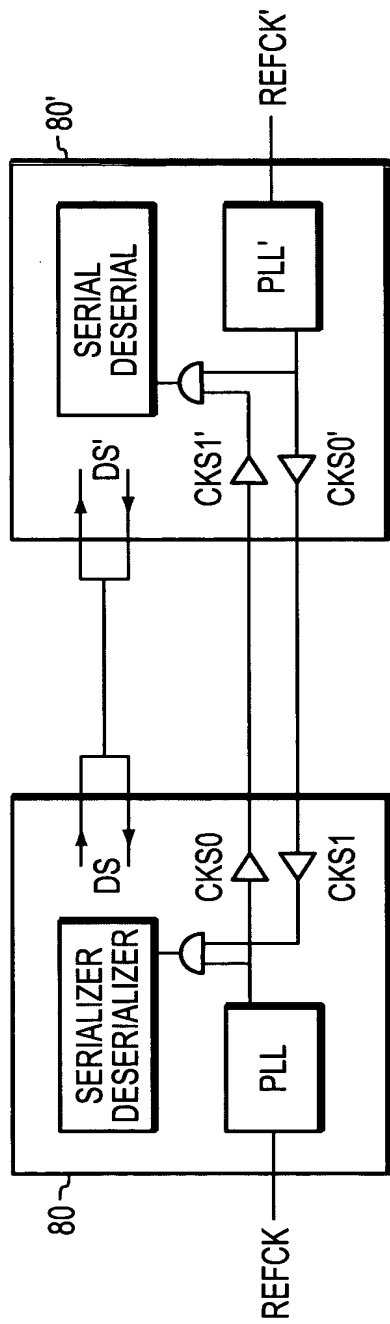


FIG. 17

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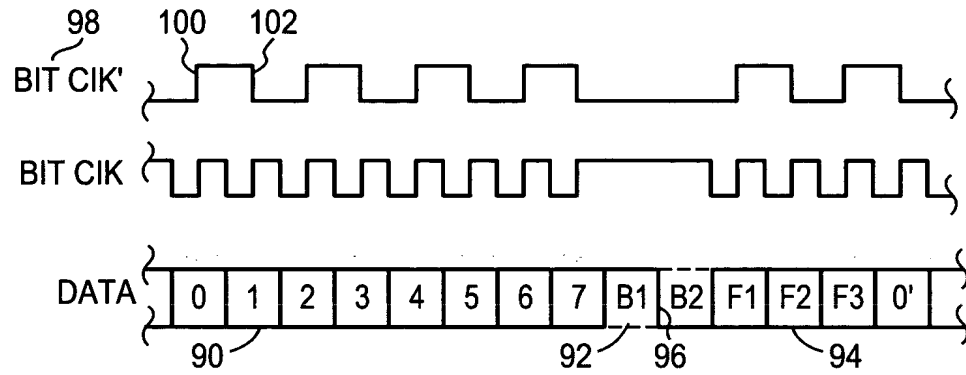


FIG. 18

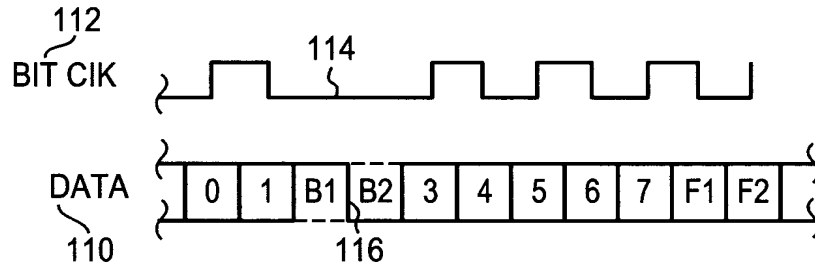


FIG. 19A

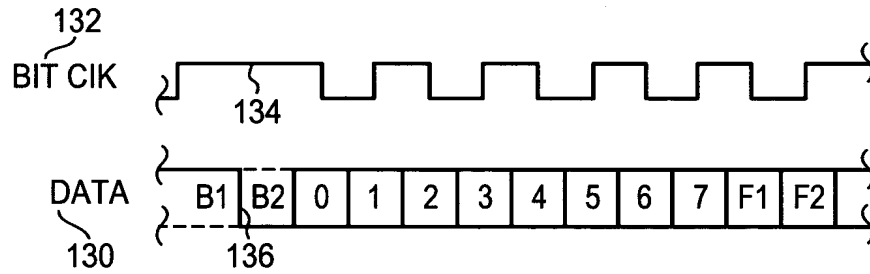


FIG. 19B

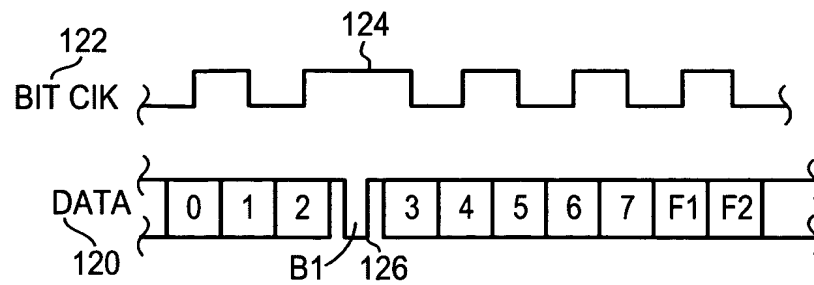


FIG. 20A

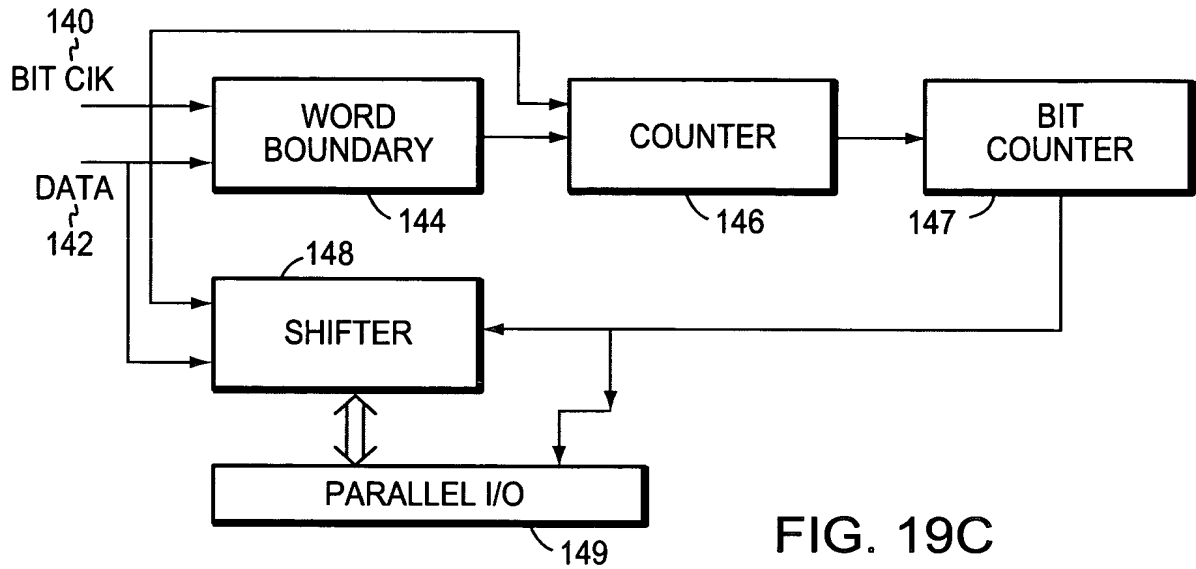


FIG. 19C

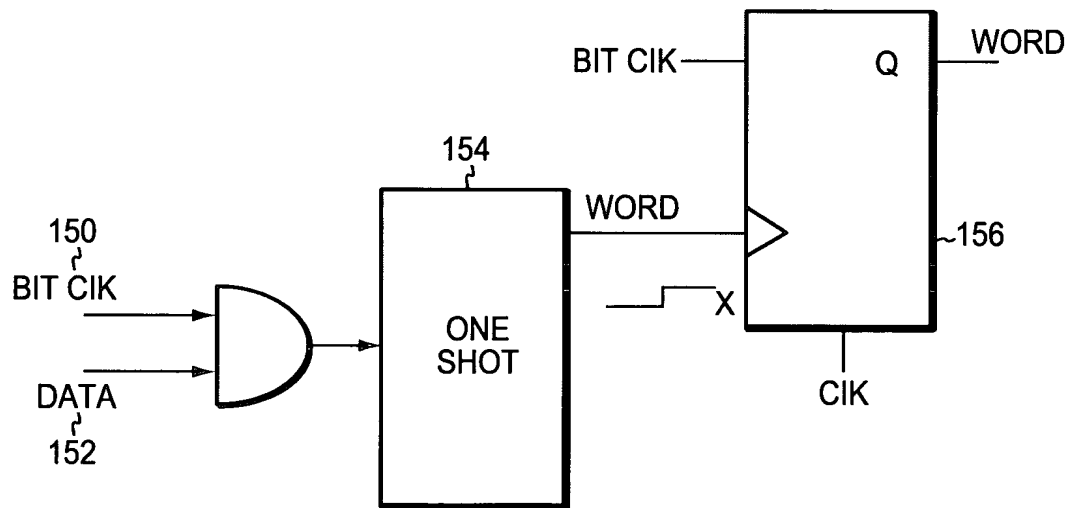


FIG. 20B